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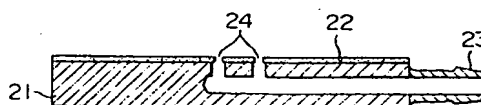
(54) Methods and apparatus for bonding bumps to leads of a TAB tape.

(57) A method of bonding bumps to leads of a TAB tape comprises: taking a substrate (22) having through-holes (24) at positions in registration with the bump positions of leads of the TAB tape, the through-holes (24) being too small to allow the bumps to pass through them, providing at one side of the substrate (22) a multiplicity of bumps too large to pass through the through-holes (24) while reducing the pressure at the other side of the substrate so as to trap the bumps in the through-holes (24), superposing a TAB tape on the substrate (22) in positional registration therewith, and pressing and heating the TAB tape by means of a bonding tool to transfer the bumps arranged on the substrate (22) to the end portions of leads of the TAB tape.

Apparatus for bonding bumps to leads of a TAB tape comprises: a substrate (22) having through-holes (24) at positions in registration with the bump positions of leads of the TAB tape, the through-holes (24) being too small to allow the bumps to be bonded to pass through them, a vacuum box (21)

upon which the substrate can be detachably mounted while a multiplicity of bumps too large to pass through the through-holes (24) are being provided at one side of the substrate (22), means for reducing the pressure in the vacuum box (21) in order to reduce the pressure at the other side of the substrate so as to trap the bumps in the through-holes (24), and a bonding tool for pressing and heating the TAB tape, when superposed on the substrate (22) in positional registration therewith, to transfer the bumps arranged on the substrate (22) to the end portions of leads of the TAB tape.

FIG. 3B



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The present invention relates to methods and apparatus for bonding bumps to leads of a TAB (Tape Automated Bonding) tape used for connection of a semiconductor chip such as an IC or LSI chip to a substrate or a lead frame.

Description of the Related Art

Conventionally, as a common practice, a semiconductor chip such as IC or LSI is supported by a lead frame and a chip circuit is connected to the lead frame by means of Au bonding wires or the like.

In recent years, however, as semiconductors implement their high performance, a demand for remarkably high density in assembling has been increased. Namely, as a result of higher density in integration of elements in an integrated circuit and a greater demand for highly developed assembly technique associated therewith, it has followed that the conventional wire bonding technique in wiring of a chip circuit cannot cope with such a demand in many cases.

A TAB system is a bonding method which copes with the above-mentioned recent demand and hence has widely been put to practical use as a method of bonding the electrodes of a semiconductor chip. In a bonding method based on the TAB system, a TAB tape having lead patterns formed in series thereon is used instead of the lead frames used in the wire bonding method and bumps preliminarily formed in either lead portions on the TAB tape or bonding portions on the side of the chip are substituted for the bonding wires. The actual bonding is generally carried out by means of a thermocompression bonding method or the like with the chip and the TAB tape being superposed on each other.

Fig. 12 is a schematic view showing one example of the TAB type of bonding. In the figure, reference numeral 1 designates a semiconductor chip on which a plurality of bumps 6 are preliminarily formed. Numeral 2 designates metallic leads which form a TAB tape 12 together with a tape-like resin film 3. The chip 1 is placed on a bonding stage 4 of a TAB bonder with the bumps 6 upside and the TAB tape is placed above the chip 1. In this state, the bonding is carried out in such a manner that a bonding tool 5 having a proper heat is pressed down from the upside in a direction indicated by arrow.

Usually, the TAB tape used in the above method, is fabricated by providing sprocket holes and a device hole in a film made of a synthetic resin such as polyimide or polyester, laminating a metal foil on the film and thereafter forming a lead pattern by means of a photoresist method.

On the other hand, the bumps for connection of the TAB tape and electrodes of the semiconductor chip such as IC or LSI are formed in many cases by applying a plating on A1 electrodes of the semiconductor chip, as disclosed by Electronic Parts and Materials, July 1989, p. 71. However, the formation of bumps on the A1 electrodes of IC or LSI through the plating method is high in cost and involves a damage of the semiconductor chip circuit during the bump forming work and hence is not necessarily recommended.

Also, as a method of forming bumps on leads on the side of a TAB tape, there is a method disclosed in JP-A-62-28623 or a "transferred bump TAB method" disclosed in National Technical Report, Vol. 31, No. 3, June, 1985, pp. 116 - 124. In the transferred bump TAB method, bumps are preliminarily placed with a predetermined pattern on a glass substrate and are transferred to leads of a TAB tape in which a lead pattern is formed. But, this method, too, involves a complicated work and a problem in cost.

SUMMARY OF THE INVENTION

As have been mentioned above, the conventional TAB system involves the problems that the bump forming method is too expensive, the bump forming work is complicated and there is a possibility of the bump forming work damaging the chip.

The present invention provides a method of bonding bumps to leads of a TAB tape, the method comprising:

taking a substrate having through-holes at positions in registration with the bump positions of leads of the TAB tape, the through-holes being too small to allow the bumps to pass through them,

providing at one side of the substrate a multiplicity of bumps too large to pass through the through-holes while reducing the pressure at the other side of the substrate so as to trap the bumps in the through-holes,

superposing a TAB tape on the substrate in positional registration therewith, and pressing and heating the TAB tape by means of a bonding tool to transfer the bumps arranged on the substrate to the end portions of leads of the TAB tape.

The method may further comprise mounting the substrate on a vacuum box, reducing the pressure within the vacuum box and bringing the vacuum box, with the substrate downwards, in proximity to the bumps so as to attract the bumps to the through-holes.

Instead, the method may further comprise mounting the substrate on the upper surface of a vacuum box, reducing the pressure within the vacuum box, and distributing the bumps over the upper surface of the substrate.

In the last mentioned case, the method may further comprise applying ultrasonic oscillation to one end of the substrate so that the bumps are trapped into the through-holes, and thereafter blowing away excess bumps remaining on the substrate.

The invention also provides apparatus for bonding bumps to leads of a TAB tape, the apparatus comprising:

a substrate having through-holes at positions in registration with the bump positions of leads of the TAB tape, the through-holes being too small to allow the bumps to be bonded to pass through them,

a vacuum box upon which the substrate can be detachably mounted while a multiplicity of bumps too large to pass through the through-holes are being provided at one side of the substrate,

means for reducing the pressure in the vacuum box in order to reduce the pressure at the other side of the substrate so as to trap the bumps in the through-holes, and

a bonding tool for pressing and heating the TAB tape, when superposed on the substrate in positional registration therewith, to transfer the bumps arranged on the substrate to the end portions of leads of the TAB tape.

The through-holes may be of constant diameter along their length.

Instead, the through-holes may be of tapered diameter along their length.

Instead, the through-holes may be of constant diameter along part of their length but terminate in a concave recess of greater diameter on the said one side of the substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a view showing an embodiment of a method of connecting a TAB tape to a semiconductor chip according to the present invention;

Fig. 1A is a perspective view of a bump sheet used in the method shown in Fig. 1;

Fig. 2 is a view for explaining an embodiment of a method of fabrication of a bumped tape according to the present invention;

Figs. 3A and 3B are views showing the structure of a jig used for fabricating the bump sheet shown in Fig. 1A and the bumped tape shown in Fig. 2;

Figs. 4A to 4D are views showing various shapes of a bump arranging through-hole provided in a plate of the jig shown in Fig. 3A;

Fig. 5 is a view showing a shape of a bump secured to each lead of a bumped tape fabricated by the method shown in Fig. 2;

Figs. 6A to 6D are views showing the structure of another jig which is used for provisionally

arranging bumps at regular positions in a method of fabricating a bumped tape.

Fig. 7 is a view showing a substrate used in another embodiment of a method of fabrication of a bumped tape according to the present invention;

Figs. 8A to 8E are views showing a method of fabricating a TAB tape by use of the substrate shown in Fig. 7;

Fig. 9 is a view showing a substrate and a synthetic resin film used in a further embodiment of a method of fabrication of a bumped tape according to the present invention;

Figs. 10A to 10F are views showing a method of fabricating a TAB tape by use of the substrate shown in Fig. 9;

Figs. 11A to 11D are views showing various manners, for comparison, in which the bumped tape of prior art and that of the present invention are each connected to a semiconductor chip.

Fig. 12 is a view showing the conventional method of connecting a TAB tape to a semiconductor chip; and

Fig. 13 is a view for explaining the conventional method of fabricating a bumped tape.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

An embodiment of a method of connecting a TAB tape to a semiconductor chip according to the present invention will now be explained with reference to the accompanying drawings.

As a material of bumps in the present invention is used a ball-like (or spherical) or massive (or block) electrically conductive material such as gold, aluminum or copper having a uniform particle size or diameter. The bumps are preliminarily arranged at positions corresponding to the end portions of leads of a TAB tape and thereafter fixed by a tape-like resin film so that the bump arrangement is not disturbed. A resin tape having the bumps arranged at proper positions is called a bump sheet.

The bump sheet is fabricated by the following method. Polyester, polyimide or the like is suitable as the material of a resin film 14 (see Fig. 1A) in which bumps are to be fixed. Bumps are fixed in a tape-like film preliminarily made of such a resin in a predetermined arrangement. As an alternative method, a bump sheet can be fabricated in such a manner that bumps are arranged and provisionally fixed on a mold or substrate, a solution of the above-mentioned resin is poured in the mold and solidified to a predetermined thickness and the solidified resin is peeled off together with the bumps from the mold. In the case of the latter method, there is a possibility that the solution of resin may cover portions of the bumps to be bon-

ded which should be exposed. In such a case, it is required that a solvent such as an alkali solution is used to dissolve the resin on surface portions of the bumps, thereby exposing metal surfaces.

Next, an example of a specific method of bonding a semiconductor chip to leads 2 on a TAB tape 12 by use of the bump sheet thus fabricated will be explained with reference to Fig. 1. A bump sheet 11 fabricated by the above-mentioned method includes a synthetic resin film 14 having bumps 6 arranged at predetermined positions, as shown in Fig. 1A. The bump sheet 11 is inserted in an accurately registered condition between a semiconductor chip 1 placed on a bonding stage 4 and lead portions 2 of a TAB tape 12. By subjecting the resultant assembly in such a state to thermocompression bonding by a bonding tool 5, electrodes 7 of the semiconductor chip 1 and the leads 2 are bonded to each other through the bumps 6 fixed on the bump sheet 11. A heating temperature in the thermocompression bonding is 350°C to 550°C and a pressure thereof is 10 to 50 g/lead. For the purpose of the bonding, the existing TAB bonder can be used as it is.

In the embodiment shown in Fig. 1, the bonding between the leads and the bumps and the bonding between the bumps and the semiconductor chip are carried out en bloc. However, in the case where the attainment of both the positional setting between the leads and the bumps and the positional setting between the bumps and the semiconductor chip is difficult in relation to positional registration or the like, the connection of the leads and the bumps and the connection of the bumps and the semiconductor chip may be carried out separately or in two steps. Namely, the bumps and the leads are first bonded with the TAB tape and the bump sheet being superposed in registration with each other and then the bumps and the electrodes of the semiconductor chip are bonded while positioning them in registration or the order of the above two steps of bonding may be inverted.

Next, explanation will be made of examples of the fabrication of a bump sheet and the connection of a TAB tape and a semiconductor chip using the bump sheet.

(Example 1)

Gold having a purity of 99.99% or more was used to form balls having a mean diameter of 80 μm , thereby providing bumps. The bumps were obtained by cutting a wire of 25 μm diameter made of a bump forming metal (gold in the shown example) into fragments of a predetermined length and heating the wire fragments upto a temperature higher than a melting point while separating them from each other so as to allow them to take spheri-

cal or ball shapes by virtue of the action of surface tension of the metal. The length of each wire fragment was selected such that the diameter of its ball-shaped version is 80 μm . The diameters of the actually formed balls fell within a range of 75 to 85 μm .

A bump sheet including such bumps arranged in registration with the positions of inner leads of a 200-pin TAB tape was fabricated by use of a jig as shown in Figs. 3A and 3B. Namely, a thin stainless steel plate 22 formed with through-holes 24 having a diameter of 70 μm slightly smaller than that of the metal bump ball at positions corresponding to the inner leads of the TAB tape was placed on the upper surface of a vacuum box 21 having a cross section as shown in Fig. 3B and the bumps were distributed on the thin plate 22 while sucking the air in the vacuum box by means of a vacuum pump (not shown) connected to a suction hole 23. Thereby, the bumps were attracted to the positions of the through-holes 24 so that they are provisionally fixed at those positions. Next, a solution of polyimide dissolved in a solvent was poured to cover the thin plate 22 and solidified to a form of a bump sheet shown in Fig. 1A. The thickness of the polyimide film was 40 μm . After the polyimide was completely solidified, it was peeled off from the thin plate 22, thereby obtaining a bump sheet which has the bumps fixed at predetermined positions.

The thus fabricated bump sheet was placed under a 200-pin TAB tape including inner leads having the same arrangement as the bumps in the bump sheet in a registration with each other, and a 200-pin Si chip was placed under the bump sheet. In such a state, the assembly was subjected to a batch thermocompression bonding by use of a TAB bonder the conditions of the thermocompression bonding were 350°C and 2 seconds.

The pull strength was examined after the bonding. Any one of the pins was cut at the lead portion of the TAB tape. Namely, it was confirmed that the bonding has a sufficient strength.

(Example 2)

Balls having a mean diameter of 90 μm (87 to 93 μm in measured values) were produced using gold of 99.99% purity as a raw material. Holes of 80 μm diameter were preliminarily provided at predetermined positions of a polyethylene sheet of 40 μm thickness by means of a laser and the gold balls heated to a certain degree were pushed into the holes. The gold balls were fixed at a central portion of the sheet, thereby forming a bump sheet.

The thus formed bump sheet was bonded by thermocompression to inner leads of a TAB tape and was thereafter bonded again by thermocom-

pression to A1 electrodes of a semiconductor chip. The bonding properties of both the bonding portions were satisfactory.

Since a tape (or bump tape) having bumps for connection preliminarily arranged at predetermined positions is fabricated and used for connecting a TAB tape to a semiconductor chip, as mentioned above, it is possible to establish a TAB assembling technique which is very low in possibility of the chip being damaged unlike the conventional method of forming bumps at electrodes of the chip and hence excellent in workability and low in cost.

In the foregoing, the TAB tape and the semiconductor chip have been connected by use of the bump sheet. However, in order to symplifying the connecting process, it is preferable to use a bumped tape in which a TAB tape and a bump sheet are integrated. As has already been mentioned, the "transferred bump TAB method" proposed by the National Technical Report, Vol. 31 No. 3 (1985), pp. 116-124 is known as a method of fabricating a bumped tape. This method will now be explained with reference to Fig. 13.

A TAB tape includes leads 2 formed on a base film 3. This TAB tape may be one which is fabricated by a usual method as will be mentioned in later. In the "transferred bump TAB method", bumps are fixed to the TAB tape as follows. As shown in Fig. 13, an electrically conductive metal 42 is uniformly deposited through an electroless plating method, a sputtering method or the like on a substrate 41 such as a glass plate. Next, a surface of the deposited metal 42 other than portions where bumps are to be formed is masked by an insulator 43. A gold plating is conducted in this state so that gold is deposited on the portions which are not masked by the insulator 43, thereby forming bumps 6. The thus formed bumps 6 are heated and pressed while positioning them in registration with the end portions of leads 2 of the TAB tape so that the bumps are transferred to the end portions of the leads 2.

According to the above method, it is generally possible to prevent a semiconductor chip from being damaged in the process of formation of bumps. Further, it is said that there is a merit that the cost for fabrication of a bumped tape is low since the bump forming substrate can be used repeatedly. In this method, however, the bump forming work itself is complicated, as is apparent from the process steps shown in Fig. 13. Also, since the bump forming substrate is damaged more and more progressively with the repeated use thereof, careful management of the substrate is required in order to always form satisfactory bumps. Further, since the bumps are once subjected to deformation when they are transferred from the bump forming substrate to the leads and

the deformed bump portions are necessarily bonded to the semiconductor chip, it involves a problem that the bonding is made with less reliability and/or less stability. Moreover, since the bumps are formed by plating, there is a great restriction in that the composition and shape of the bump cannot be selected freely. Especially, as to the composition of the bump, though an alloy added with a special element is superior to pure gold in many cases where the property of bonding between the pads or electrodes of a semiconductor chip and the bumps is especially important, it is not possible to form such special-alloy bumps by the conventional method using the plating technique.

As regards the fabrication of a TAB tape itself, a variety of methods have already been established and good products are supplied. A two-layer tape and a three-layer tape are especially very useful among them. A difference between the two-layer and three-layer tapes is as follows. Namely, the two-layer tape is one in which a metal (copper in many cases) is plated on a film base of the tape, so as to form a lead pattern directly. On the other hand, the three-layer tape is one in which a metal foil is covered on a film base through an adhesive and a lead pattern is thereafter formed by etching. A synthetic resin such as polyimide or polyester is used as a base film of the TAB tape. Also, a product of such a film having sprocket holes and a device hole preliminarily formed therein is available as a base film for a TAB tape.

A bumped tape according to the present invention is fabricated in such a manner that preliminarily prepared ball-like bumps are provisionally arranged on a substrate at positions corresponding to electrodes of a semiconductor chip. The substrate and a TAB tape are thereafter superposed in positional registration with each other and then the bumps provisionally arranged on the substrate are transferred to the end portions of leads of the TAB tape. Therefore, portions of the bumps to be bonded to the semiconductor chip are subjected to no deformation so that each bump keeps substantially its original spherical or ball shape. As a result, satisfactory bonding to the semiconductor chip is attainable.

A method of fabrication of a bumped tape according to the present invention will now be explained with reference to Fig. 2.

A TAB tape may be any of an existing two-layer or three-layer TAB tape in which a predetermined pattern of leads 2 is formed on a base film 3. A substrate 31 which may be a thin sheet made of stainless steel is prepared for providing an array of bumps. Recesses or through-holes meeting to the sizes of ball-like bumps are provided in the substrate 31 at positions corresponding to leads of the TAB tape. By dropping the bumps into the

respective recesses or through-holes, the bumps are arranged at predetermined positions. The used ball-like bumps are formed in a similar manner to the formation of bumps used for fabrication of a bump sheet as mentioned above. The material and size of the bump are properly selected in accordance with the material components of a TAB tape and a semiconductor chip to be connected.

The TAB tape and the substrate having the bumps thus arranged at the predetermined positions are superposed one another positional registration with each other such that the bumps of the substrate and the lead end portions of the TAB tape are aligned with each other. Then, the bumps are heated and pressed at a temperature of 350 to 550 °C under a pressure of 10 to 50 g/lead so that the bumps are transferred to the end portions of the leads. At this time, a portion of the bump to be bonded to the lead is subjected to deformation due to the pressing but a head portion thereof to be bonded to the electrode of the semiconductor chip is not substantially subjected to deformation or maintains its original soft ball shape. Therefore, satisfactory bonding to the semiconductor chip is attainable.

Like the case of the existing bumped tape, the fabricated bumped tape and the semiconductor chip are superposed one another with the bumps and the electrodes of the semiconductor chip being in positional registration with each other and are bonded by a thermocompression bonding method or the like.

Next, various examples of the fabrication of a bumped tape will be explained in detail.

(Example 3)

A vacuum box 21 shown in Fig. 3A was attached on a bonding stage of a TAB bonder. A thin stainless steel plate 22 of 0.1 mm thickness is detachably mounted on the upper surface of the vacuum box 21 and the interior of the vacuum box 21 is adapted to be pressure-reduced by a vacuum pump (not shown) connected to a suction hole 23.

The stainless steel plate 22 of 0.1 mm thickness, which serves as a bump forming substrate, includes through-holes 24 the number of which is 200 in total, each of which has a diameter of 70 μm and which are formed by etching at positions in registration with the bump positions of inner leads of a 200-pin TAB tape. The cross section of the through-hole was a simple shape as shown in Fig. 4A. A multiplicity of balls having a mean diameter of 80 μm and made of gold having a purity of 99.99% were distributed on an upper surface of the bump forming substrate while pressure-reducing lower part under the substrate. At this time, ultrasonic oscillation was applied to one end of the

substrate so that the gold balls were trapped into the through-holes. Thereafter, excess gold balls remaining on the substrate were readily blown away by air.

Next, a TAB tape was superposed on the substrate in positional registration with each other and was pressed and heated by means of a bonding tool (not shown), thereby transferring the gold balls arranged on the substrate to the end portions of leads of the TAB tape. It was possible to repeatedly use the substrate, on which gold balls were distributed again.

The bump transferred to the lead end portion was deformed into a shape having a cross-section as shown in Fig. 5A as the result of the transfer process. However, the tip of the bump maintains its spherical ball shape convenient for bonding to the semiconductor chip.

(Example 4)

A thin stainless steel plate 22 on the upper surface of the vacuum box 21 shown in Fig. 3A was prepared with through-holes, each having a cross section as shown in Fig. 4B. Such inclined or tapered through-holes were obtained by laser beam machining. The interior of the vacuum box 21 was pressure-reduced so that ball-like bumps dropped into the through-holes in a similar manner to that in the Example 3.

Next, a TAB tape was superposed on the thin plate or substrate in positional registration with each other and was heated and pressed by means of a bonding tool so that the gold balls arranged on the substrate were transferred to the end portions of leads of the TAB tape. In the case of the present example, since the through-hole of the substrate is tapered, there is a possibility that the peeling of the leads to which the bumps have been transferred from the substrate is easy and hence the possibility of the leads being deformed is less. The cross section of the bump as transferred is not substantially different from that in the Example 3. Bonding to a semiconductor chip was carried out with a very good result.

(Example 5)

Balls having a diameter of 40 μm were produced by use of an alloy of low-melting temperature containing 88% of gold and 12% of germanium.

A vacuum box similar to that in the Example 3 was used and a substrate on the vacuum box was provided with through-holes each having a cross-sectional shape as shown in Fig. 4C. The through-hole was produced by forming a concave recess in an upper surface of the substrate by use of a

punch having a trigonal pyramid head and thereafter irradiating a bottom portion of the recess with a laser beam. The size of the through-hole was determined such that the maximum diameter portion at the upper side of the through-hole was about 30 μm .

After the gold-germanium alloy balls were distributed on the substrate, of which the inner chamber was pressure-reduced so that they were trapped into the through-holes, a TAB tape was superposed on the substrate in positional registration with each other and the bumps were transferred to the end portions of leads of the TAB tape by means of a bonding machine. In the present example, the pulling-up of the leads after the transfer of the bumps thereto was much easier as compared with the case of the Example 4. In the case where the affinity between the bump forming substrate and the bumps is so strong due to a compositional relation therebetween that the pulling-up of the leads having the bumps as transferred from the substrate is difficult, the provision of the substrate through-holes shaped as in the present example is convenient. Also the through-holes having cross-sectional shapes as shown in Fig. 4D, provide substantially the same effect as that in the present example.

(Example 6)

Like the Example 4, a thin stainless steel plate 21 on an upper surface of a vacuum box shown in Fig. 3A was formed with through-holes having cross-sectional shapes as shown in Fig. 4B. A soft rubber tube was coupled to a suction hole of the vacuum box so as to allow the vacuum box to freely move in a state that the interior of the vacuum box is pressure-reduced.

In addition to the thin plate or substrate, a flat plate was prepared. Ball-like gold bumps were randomly distributed on the flat plate. The vacuum box was brought in proximity to the gold bumps on the flat plate in a state the vacuum box is turned over with the substrate down side, so that the bumps are attracted to the through-holes as pressure-reduced. Next, the vacuum box having the bumps attracted thereto was again turned over and fixed on a bonding stage of a TAB bonder. a TAB tape was superposed on the substrate of the vacuum box in positional registration with each other and the bumps were transferred to lead portions of the TAB tape.

In the case of the present example, since bumps are attracted to only the through-holes of the substrate from the beginning, an operation of blowing away excess bumps on the substrate was unnecessary.

(Example 7)

In the present example, no vacuum box was used but a bump forming substrate of a double structure having a cross section as shown in Fig. 6A was used. An upper substrate 22a, made of a stainless steel plate having a thickness of 50 μm was provided with through-holes of an inner diameter of 100 μm slightly larger than the diameter 80 μm of ball-like gold bumps 6 at positions corresponding to a pattern of electrodes of a semiconductor chip. A lower substrate 22b made of a stainless steel having a thickness of 200 μm was provided on its surface with disk-shaped recesses of 10 μm depth at positions corresponding to the through-holes of the upper substrate.

The two substrates were superposed with two stainless steel plates or spacers (not shown) of 20 μm thickness being interposed between the opposite edges of the two substrates so that a gap of about 20 μm is formed between the two substrates, as shown in Fig. 6A. The bumps were distributed on the substrate 22a and ultrasonic oscillation was applied to drop the bumps into the through-holes of the substrate 22a. The dropped bumps were supported by the recesses of the lower substrate 22b, as shown in Fig. 6B. But excess bumps, which were not trapped into the through-holes, were left on the substrate 22a. Therefore, a sweeping plate 45 was let run on the upper surface of the substrate 22a to remove the excess bumps, as shown in Fig. 6C.

Next, the spaces were removed and the substrate 22b was moved upward until the gap between the substrates 22a and 22b disappears. Thereby, the bumps were provisionally positioned regularly with the upper portion of each bump being protruded by about 20 μm from the upper surface of the substrate 22a, as shown in Fig. 6D. Leads of a TAB tape were superposed on the provisionally arranged bumps in positional registration with each other and then the bumps were transferred to the end portions of the leads, in a similar manner to the foregoing examples, thereby fabricating a bumped tape.

According to the present example, since no pressure reduction by a vacuum equipment or the like is not required, the workability is more facilitated. Further, since the substrate to which a pressure is applied by a bonding tool when bonding can be made as thick as possible (200 μm in the present example), a fabricating equipment having a durability can be provided.

(Example 8)

Balls having a mean diameter of 80 μm were produced as bumps by use of gold having a purity

of 99.99% or more. The diameters of the actual balls were within a range of 75 to 85 μm .

The gold bumps were arranged at positions meeting the positions of inner leads of a 200-pin TAB tape. The arrangement was carried out in a manner as explained below with reference to Figs. 7 and 8A to 8E. There was prepared a mold substrate 22 (see Fig. 7) made of a thin stainless steel plate and formed with through-holes 24 having a diameter of 70 μm slightly smaller than the diameter of the gold balls at positions corresponding to the inner leads of the TAB tape. Fig. 8A shows a cross section taken along line VIII-VIII of Fig. 7. The through-holes 24 of the substrate 22 were sucked from the lower side by means of a vacuum pump so that the bumps 6 were attracted to and provisionally fixed at the positions of the through-holes, as shown in Fig. 8B. Next, a solution of polyimide dissolved by a solvent was poured and solidified, as shown in Fig. 8C, so that the thickness of the polyimide film 40 after solidification becomes 40 μm . After the polyimide was completely solidified, it was peeled off from the mold, thereby obtaining a bump sheet 11, as shown in Fig. 8D, in which the bumps 6 were fixed at predetermined positions.

In the obtained bump sheet, a surface of the bump (especially, the upper side thereof) was partially coated with polyimide. Therefore, the surface was lightly etched by means of caustic soda to expose a metal surface of the top portion of the bump.

Next, a pattern of leads 2 having a thickness of about 30 μm were formed directly on one surface of the bump sheet by means of copper-plating, as shown in Fig. 8E, thereby completing a bumped tape.

This bumped tape was placed in positional registration with a semiconductor chip having 200 electrodes in the same arranged as the leads of the TAB tape and the assembly was subjected to thermocompression bonding by a TAB bonder. The bonding conditions were such that the temperature is 350 °C, the pressure is 30 g/lead and the time is 2 seconds.

The pull strength was examined after bonding and it was confirmed that any one of the pins is cut at the inner lead portion of the TAB tape and hence the bonding has a sufficient strength.

A usual TAB tape is fabricated such that a portion of the tape where a semiconductor chip is to be located is bored in the form of a window as a device hole and only the end portions of leads extends into inside of the device hole. The bonding of the usual TAB tape to the semiconductor chip is carried out, as shown in Fig. 11A. In the figure, reference numeral 2 designates leads, numeral 33 a base film of the TAB tape, numeral 1 the semi-

conductor chip, numeral 6 bumps, and numeral 50 a bonding tool. In the arrangement as shown in Fig. 11A, the bonding is impossible unless the device hole is provided. In the case where no device hole is formed in conventional bumped tape, the bonding is carried out, as shown in Fig. 11B. In that case, however, no practical bonding is effected since a heated bonding tool contacts directly a base film of the tape so that there are encountered inconveniences due to the burning of the film resulting in the deterioration of an insulating ability thereof and the contamination of the bonding tool surface.

As apparent from the above, a device hole is indispensable to the conventional bumped tape. Therefore, in the case of a two-layer type tape, a device hole is bored by etching a film after leads have been formed. In the case of a three-layer tape, a device hole is preliminarily bored together with sprocket holes by means of punching or the like prior to the formation of leads.

In the case of a bumped tape according to the present invention, bumps are provided to extend into a base film. Therefore, as understood from Fig. 11C, even if a tape having no device hole bored therein is used as it is, there is no possibility that a bonding tool of a bonder contacts directly a film. In other words, a step for formation of a device hole can be omitted from a tape fabricating process. This is one additional merit showing that the workability of a method according to the present invention is excellent.

(Example 9)

Balls having a mean diameter of 90 μm (87 to 93 μm in measured values) were produced using gold of 99.99% purity as a raw material. 80 μm holes were bored in a polyimide film of 40 μm thickness by a laser at positions corresponding to the positions of the electrodes of a 200-pin semiconductor chip. The gold balls were fixed into the holes. The fixing method will now be explained by using Figs. 9 and 10A to 10F.

First, as shown in Fig. 9, there was prepared a substrate including a pile of a polyimide film 40 formed with holes at positions where the gold balls are to be arranged and a thin stainless steel plate 22 which is placed on the rear side of the film 40 and provided with holes formed at the same positions as those formed in the film 40. Fig. 10A shows a cross section taken along line XA-XA of Fig. 9. As shown in Fig. 10B, the rear side of the tin stainless steel plate 22 was evacuated so that the gold balls 6 are attracted to the hole positions of the polyimide film 40. Next, as shown in Fig. 10C, the gold balls were mechanically pushed from the front side of the film 40 into the holes of the

plate 22 by means of a press 53 so that the head portions of the balls were protruded by about 5 μm from the opposite surface of the tape. In this manner, a bump sheet as shown in Fig. 10D was obtained in which the gold bump was fixed or positioned asymmetrically with respect to the tape surface.

Next, one surface of this bump sheet or the sheet surface on the side having the bumps protruded by about 5 μm therefrom was copper-plated to form leads 2 in a predetermined pattern, as shown in Fig. 10E. The thickness of the copper plating was in order of 30 μm . Thereby, the gold bumps and the copper leads were integrated with good electrical conductivity therebetween as well as a mechanical bonding having a certain degree of strength. As shown in Fig. 10F, a device hole in the form of window was etched in the tape at a central portion of the bump sheet for locating there a semiconductor chip to be connected. Thereby, a bumped tape was obtained in which the end portions of the leads were extending inside of the device hole and the ball-like gold bumps were secured to the end portions of the leads.

The bumped tape fabricated by the above-mentioned method was used to conduct the test of connection or bonding to a semiconductor chip. The thermocompression bonding conditions were such that the pressure was 35 g/lead, the temperature was 450°C and the time was 1 second. As the result of examination of the pull strengths of the lead bonding portions, any one was cut at the lead portion. This shows that the bonding between the leads and the bumps as well as the bonding between the bumps and the semiconductor chip were satisfactory.

In the present example, a device hole was formed in a portion of the tape for locating there the semiconductor chip to be connected, like the case of the conventional bumped tape fabricated by the "transferred bump TAB method". In the present example, though an additional step of forming the hole is required, the bonding can be conducted in a manner as shown in Fig. 11D. Therefore, the heating conditions for bonding can be selected freely, thereby making it possible to realize more satisfactory bonding. Also in the case of the present example, since the bump has a ball-like shape, there was confirmed a merit that the bumps encounter a very smooth contact with electrode portions of the semiconductor chip upon actual bonding so that no excessive load is imposed upon the chip.

(Example 10)

Holes of 80 μm diameter were bored in a polyester film tape of 40 μm thickness and gold

balls of 99.99% purity having a mean diameter of 90 μm were pushed into the holes. The gold balls were arranged on the tape at positions corresponding to the electrodes of a 100-pin semiconductor chip to be connected thereto. The gold balls were fixed on the tape in the same method as the Example 9. Namely, the gold balls were fixed such that each ball is protruded by about 5 μm from one surface of the tape and by about 45 μm from the opposite surface thereof.

Next, on the one surface of this bump sheet or the sheet surface having the bumps protruded by about 5 μm therefrom, a copper foil of 35 μm thickness was fixed by means of an adhesive. Though the head of the bump is slightly protruded, this involved no hindrance to the adhesion of the copper foil. This copper foil was etched to form a pattern of leads corresponding to the positions of electrodes of a 100-pin semiconductor chip. Thereafter, the base film was etched to form a device hole, thereby fabricating a bumped tape.

In the case of the present example, it does not always provide a satisfactory bonding between the bumps and the leads made in the above fabricating process.

However, when a semiconductor chip has been bonded to the leads of a bumped tape fabricated by the above process, the bonding between the bumps and the leads becomes satisfactory to attain sufficient electrical/mechanical bonding properties.

Claims

1. A method of bonding bumps to leads of a TAB tape, the method comprising:

taking a substrate (22) having through-holes (24) at positions in registration with the bump positions of leads of the TAB tape, the through-holes (24) being too small to allow the bumps to pass through them,

providing at one side of the substrate (22) a multiplicity of bumps too large to pass through the through-holes (24) while reducing the pressure at the other side of the substrate so as to trap the bumps in the through-holes (24),

superposing a TAB tape on the substrate (22) in positional registration therewith, and pressing and heating the TAB tape by means of a bonding tool to transfer the bumps arranged on the substrate (22) to the end portions of leads of the TAB tape.

2. A method as claimed in claim 1, further comprising mounting the substrate (22) on a vacuum box (21), reducing the pressure within the vacuum box (21) and bringing the vacuum box (21), with the substrate (22) downwards, in

proximity to the bumps so as to attract the bumps to the through-holes (24).

3. A method as claimed in claim 1, further comprising mounting the substrate (22) on the upper surface of a vacuum box (21), reducing the pressure within the vacuum box (21), and distributing the bumps over the upper surface of the substrate (22). 5
4. A method as claimed 3, further comprising applying ultrasonic oscillation to one end of the substrate (22) so that the bumps are trapped into the through-holes (24), and thereafter blowing away excess bumps remaining on the substrate (22). 10 15
5. Apparatus for bonding bumps to leads of a TAB tape, the apparatus comprising:
 - a substrate (22) having through-holes (24) at positions in registration with the bump positions of leads of the TAB tape, the through-holes (24) being too small to allow the bumps to be bonded to pass through them, 20
 - a vacuum box (21) upon which the substrate can be detachably mounted while a multiplicity of bumps too large to pass through the through-holes (24) are being provided at one side of the substrate (22), 25
 - means for reducing the pressure in the vacuum box (21) in order to reduce the pressure at the other side of the substrate so as to trap the bumps in the through-holes (24), and 30
 - a bonding tool for pressing and heating the TAB tape, when superposed on the substrate (22) in positional registration therewith, to transfer the bumps arranged on the substrate (22) to the end portions of leads of the TAB tape. 35
6. Apparatus as claimed in claim 5, wherein the through-holes (24) are of constant diameter along their length. 40
7. Apparatus as claimed in claim 5, wherein the through-holes (24) are of tapered diameter along their length. 45
8. Apparatus as claimed in claim 5, wherein the through-holes (24) are of constant diameter along part of their length but terminate in a concave recess of greater diameter on the said one side of the substrate (22). 50

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FIG. 1

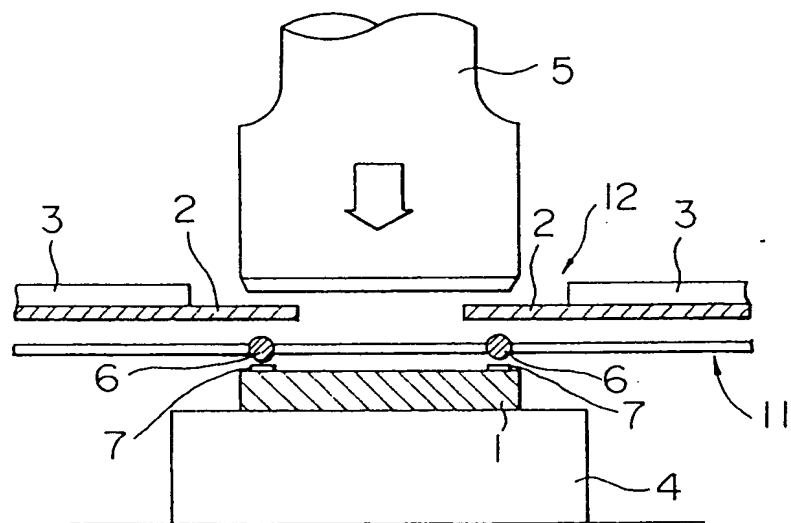


FIG. 1A

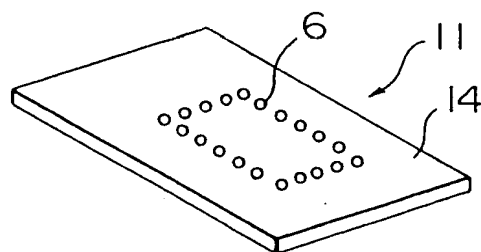


FIG. 2

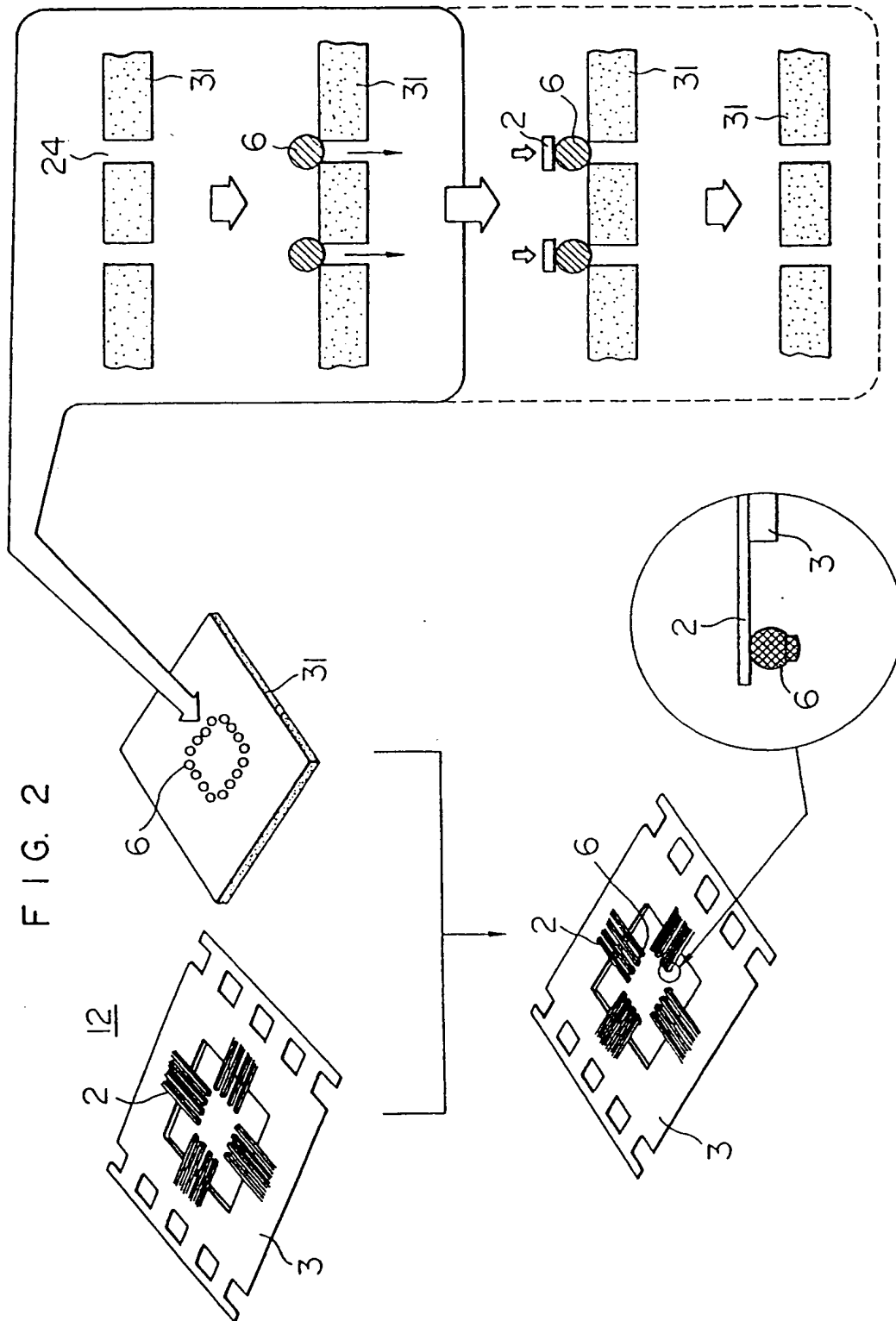


FIG. 3A

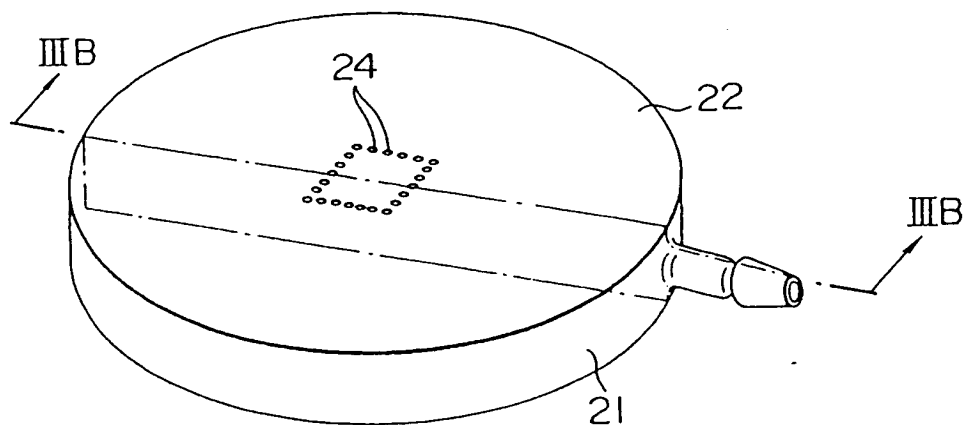


FIG. 3B

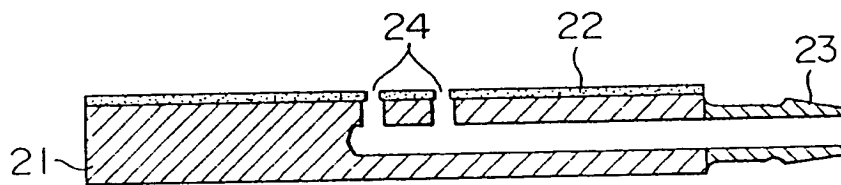


FIG. 4A

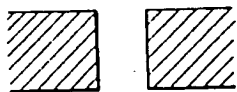


FIG. 4B

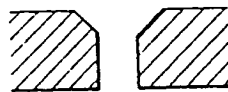


FIG. 4C

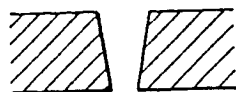


FIG. 4D

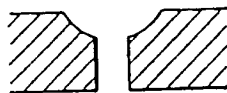


FIG. 5

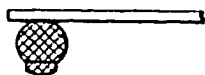


FIG. 6A

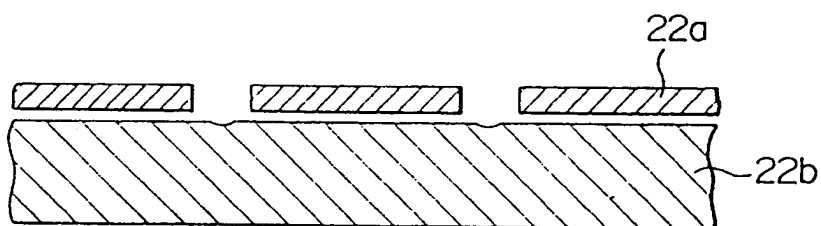


FIG. 6B

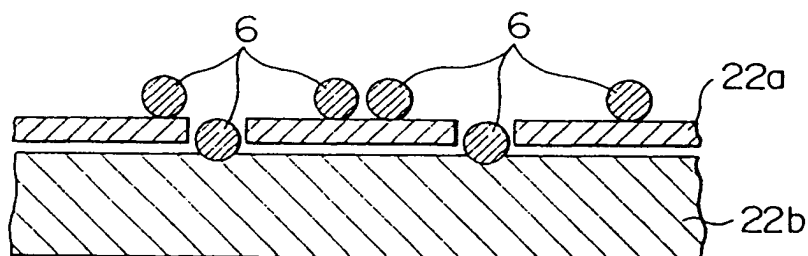


FIG. 6C

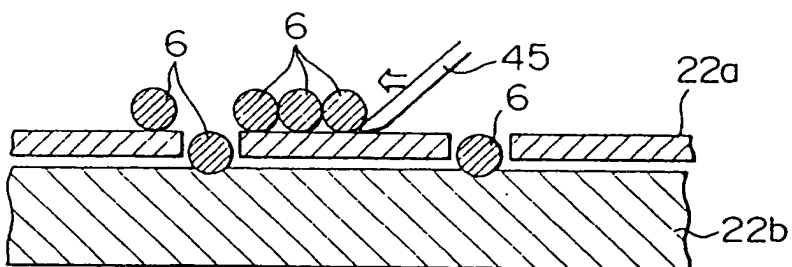


FIG. 6D

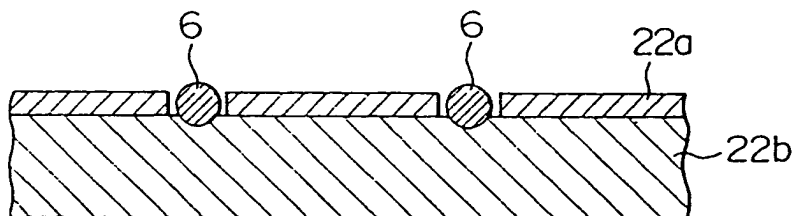


FIG. 7

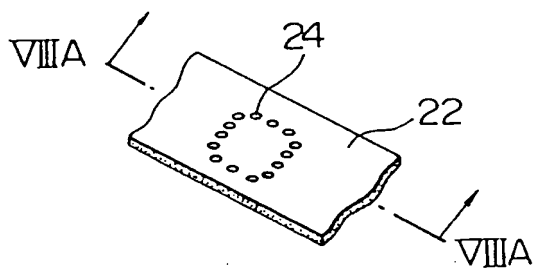


FIG. 8A



FIG. 8B

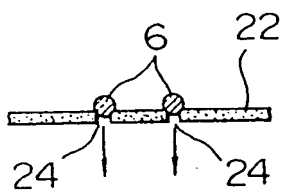


FIG. 8C

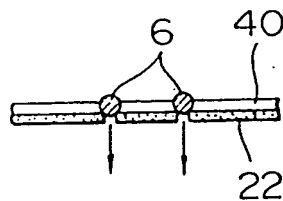


FIG. 8D

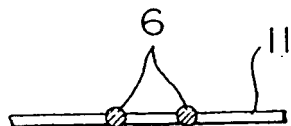


FIG. 8E

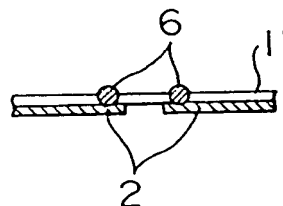


FIG. 9

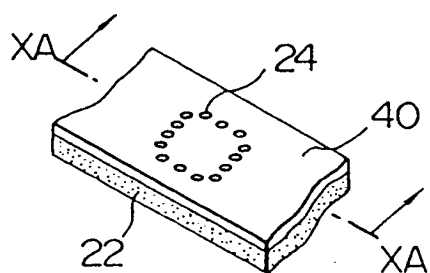


FIG. 10A

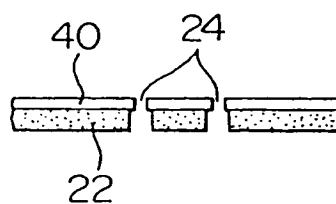


FIG. 10B

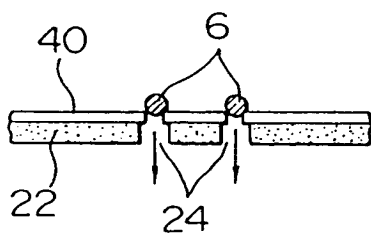


FIG. 10C

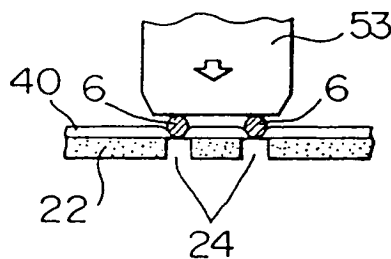


FIG. 10D

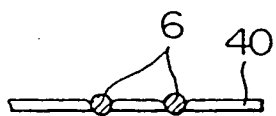


FIG. 10E

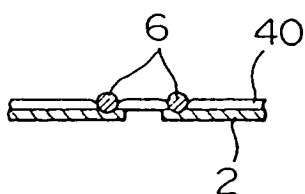


FIG. 10F

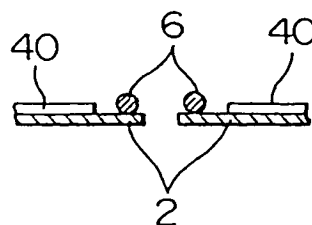


FIG. IIA

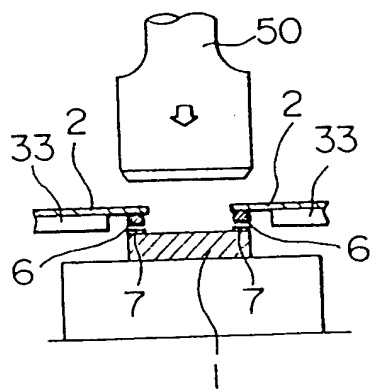


FIG. IIB

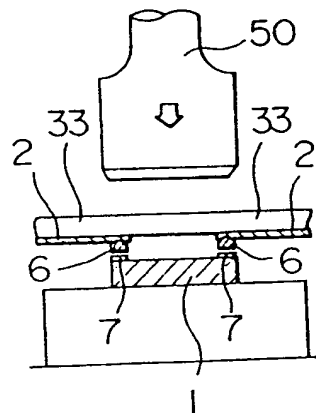


FIG. IIC

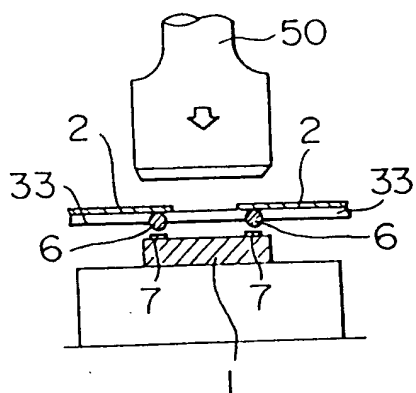


FIG. IID

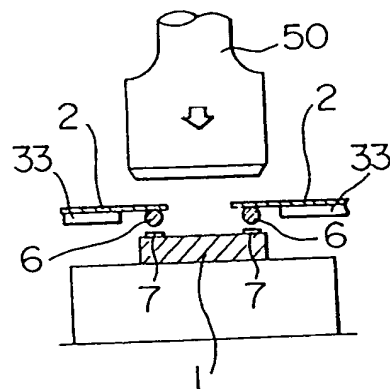


FIG. 12 PRIOR ART

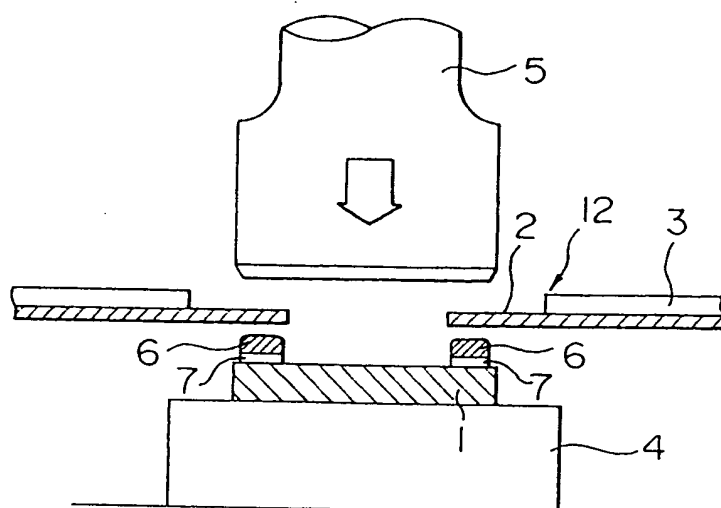
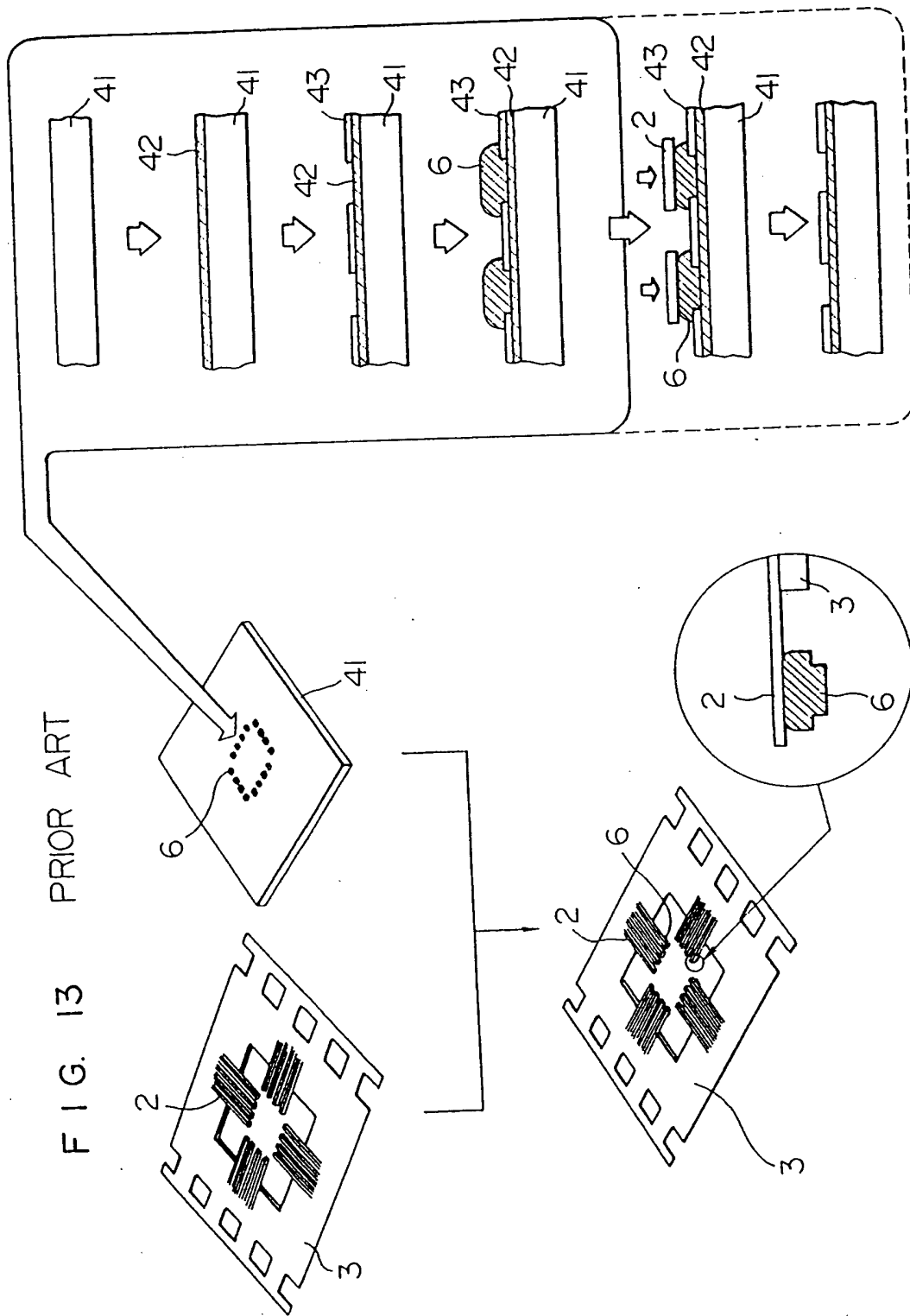


FIG. 13 PRIOR ART





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

EP 92 11 2860

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y	EP-A-0 307 591 (HITACHI LTD) * column 1, line 49 - column 2, line 9 * * column 3, line 42 - column 4, line 4 * * figures 1-3 *	1,3,5,6	H01L21/603 H01L21/48
Y	--- IEEE TRANSACTIONS ON COMPONENTS, HYBRIDS, AND MANUFACTURING TECHNOLOGY vol. 10, no. 3, September 1987, NEW YORK US pages 336 - 340; HATADA ET AL: 'New Film Carrier Assembly Technology: Transferred Bump TAB' * figure 1 * * page 335, right column, paragraph 3 * * page 336, left column, paragraph 5 *	1,3,5,6	
A	--- PATENT ABSTRACTS OF JAPAN vol. 2, no. 1 (E-001)5 January 1978 & JP-A-52 115 176 (HITACHI SEISAKUSHO) 27 September 1977 * abstract *	1,5	
A	--- PATENT ABSTRACTS OF JAPAN vol. 13, no. 378 (E-809)22 August 1989 & JP-A-1 129 446 (FUJITSU LTD) 22 May 1989 * abstract *	2	TECHNICAL FIELDS SEARCHED (Int. Cl.5) H01L

The present search report has been drawn up for all claims

Place of search

THE HAGUE

Date of completion of the search

18 SEPTEMBER 1992

Examiner

GREENE S. K.

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
Y : particularly relevant if combined with another document of the same category
A : technological background
O : non-written disclosure
P : intermediate document

T : theory or principle underlying the invention
E : earlier patent document, but published on, or after the filing date
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